ABSTRACT OF THE DISCLOSURE

A coding parameter control circuit 31 computes frame length from bit rate and coding delay, and provides the computed frame length data to a CELP coding circuit 32. On the basis of the computed frame length, the coding parameter control circuit 32 selects control parameters from a table, in which a plurality of control parameters for controlling the operation of the CELP coding circuit are set, on the basis of the bit rate, and provides the selected control parameters to the CELP coding circuit. The coding parameter control circuit provides the sub-frame length, and bit number distributed to the multi-pulse signal to the multi-pulse signal generation parameter setting circuit 33. The multi-pulse signal coding parameter setting circuit 33 computes pulse number of multi-pulse excitation signal, pulse position candidates of each pulse and candidate positions thereof from the sub-frame length and bit number of multi-pulse signal.